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09/738,900	12/18/2000	Heung-bac Lee	Q61824	2194

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EXAMINER

GHEBRETINSAE, TEMESGHEN

ART UNIT	PAPER NUMBER
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2611

SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE
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**BEFORE THE BOARD OF PATENT APPEALS
AND INTERFERENCES**

Application Number: 09/738,900
Filing Date: December 18, 2000
Appellant(s): LEE, HEUNG-BAE

Peter A. McKenna
For Appellant

EXAMINER'S ANSWER

MAILED
APR 17 2007
GROUP 2600

This is in response to the appeal brief filed December 15, 2006 appealing from the
Office action mailed 12/13/05.

(1) Real Party in Interest

A statement identifying by name the real party in interest is contained in the brief.

(2) Related Appeals and Interferences

The examiner is not aware of any related appeals, interferences, or judicial proceedings which will directly affect or be directly affected by or have a bearing on the Board's decision in the pending appeal.

(3) Status of Claims

The statement of the status of claims contained in the brief is correct.

(4) Status of Amendments After Final

The appellant's statement of the status of amendments after final rejection contained in the brief is correct.

(5) Summary of Claimed Subject Matter

The summary of claimed subject matter contained in the brief is correct.

(6) Grounds of Rejection to be Reviewed on Appeal

The appellant's statement of the grounds of rejection to be reviewed on appeal is correct.

(7) Claims Appendix

The copy of the appealed claims contained in the Appendix to the brief is correct.

(8) Evidence Relied Upon

No evidence is relied upon by the examiner in the rejection of the claims under appeal.

(9) Grounds of Rejection

The following ground(s) of rejection are applicable to the appealed claims:

Claim Rejections - 35 USC § 103

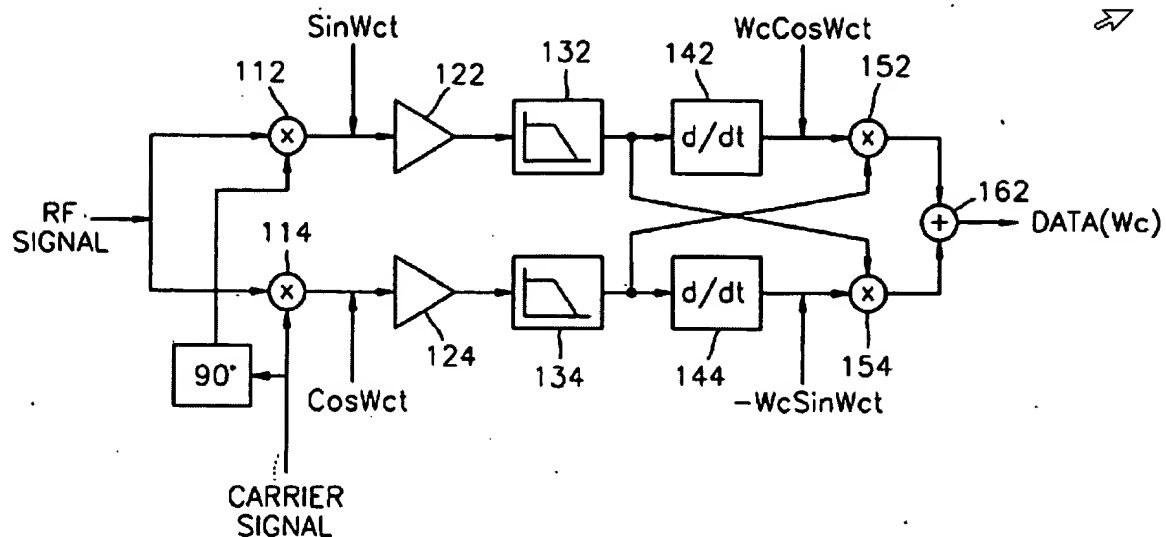
The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

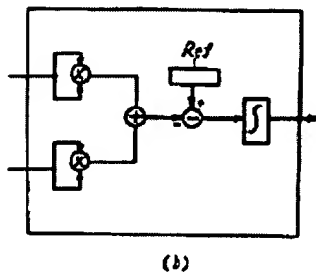
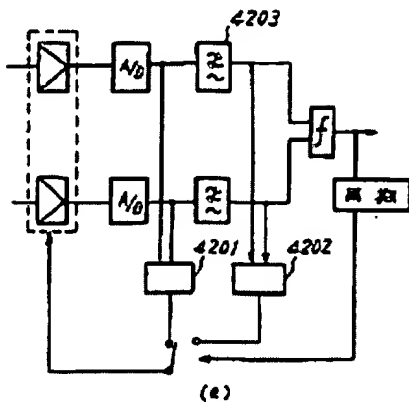
Claims 1 -3 are rejected under 35 U.S.C. 103(a) as being unpatentable over the admitted prior art in view of Hiroshi (JP-6244754)

The prior art (fig.1) discloses a direct conversion demodulator in RF reception system comprising: a down mixer (112,114) for mixing a received RF signal and a carrier signals, and thereby converting the RF signal into baseband signals of channels I and Q.; a filter (132,134) for filtering high frequency component of the baseband signal; a differentiator (142,144) for differencing the baseband signal of the two channels output from the filter; a multiplier (152,154) for cross multiplying the baseband signals of the two channels output from the differentiator and the baseband signal of the two channels output from the filter; an adder (162) for adding the baseband signal of the two channels output by the multiplier and thereby detecting data.

FIG. 1 (PRIOR ART)

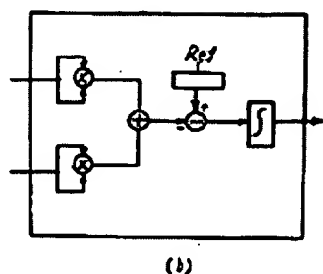


The prior art is different from the claimed invention in that a detector for detecting a gain control level and an AGC for controlling gains (270) as claimed in claims 1 and 3 is missing or not disclosed. However, Hiroshi (JP-6244754) discloses a receiver comprising down mixer (109) for mixing a received RF signal and carrier signal; a filter (110,111) for filtering high frequency components of the baseband signal; **a detector for detecting a gain control level corresponding to the difference obtained by comparing the levels of the baseband signal of the two channels output by the filter with a predetermined level (see fig.27 (a) and (b));** an AGC for controlling gains of the baseband signals for each of the two channels output from the down mixer according to the gain control level detected by the detector.



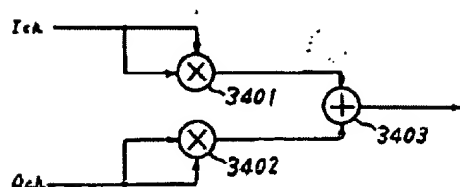
Thus, it would have been obvious to one of ordinary skill in the art at the time the invention was made to use the detector and AGC control 4202 (fig.27 (a), (b)) of Hiroshi in the system of the prior art so that the output signal of the prior art is maintained at a constant level and the data is detected correctly.

The detector of Hiroshi also discloses a multiplying unit for multiplying the baseband signals of the two channels with themselves; an adding unit for adding the baseband signals of the two channels multiplied by the multiplying unit and a level comparing unit for generating a gain control level corresponding to the difference obtained by comparing the levels of the signals detected by the adding unit with a predetermined level (see fig. 27(b)) as claimed in claim 2.



(b)

[FIG 18]



(10) Response to Argument

Applicant's arguments filed 12/15/06 have been fully considered but they are not persuasive.

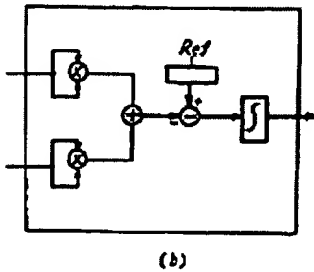
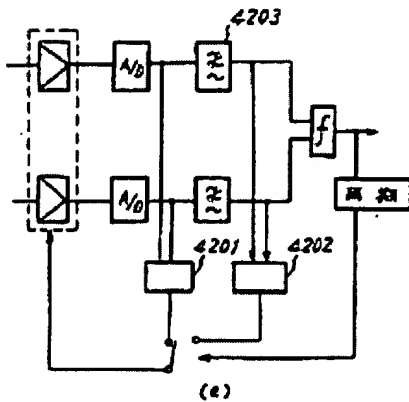
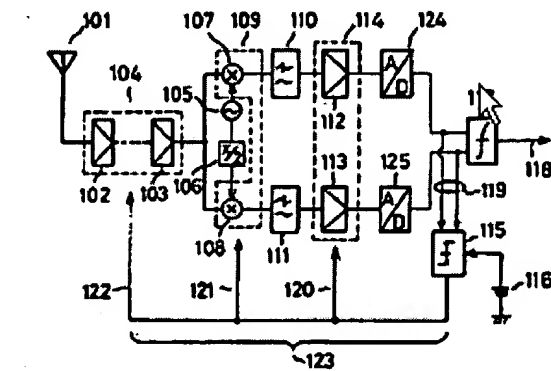
Applicant argues that fig. 27(b) shows a gain control circuit 4202 that outputs a signal based upon an integrated value of a difference between a reference signal and the sum of the two other signals. This integrated value is applied to the amplifiers and is not *analogous* to the claimed "gain control level corresponding to the difference obtained by comparing the levels of the baseband signals of the two channels output by the filter with a predetermined level" and the AGC controlling gains according to the gain control level detected by the detector. That is, because the output of element 4202 is an integrated value, it does not correspond to the output of the subtractor shown in fig.27 (b).

Examiner disagree with applicant's conclusion because Hiroshi does disclose

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a detector (4202 (a) and 4202 (b)) for detecting a gain control level corresponding to the difference obtained by comparing the levels of the baseband signals of the two channels (4203) by filter with a predetermined level. (See fig. 27 (a) and (b)) and Fig.1 (elements 115,116,119).

【図 1】



Hiroshi further shows that the output from the subtractor being integrated. It is well settled, however, that omission of an element and its function in a combination is an obvious expedient if the remaining elements perform the same functions as before (In re Nelson, 40 CCPA 708).

Omission of an Element and its function is obvious if the function of the element is not desired (Ex parte Wu, 10 USPQ 2031); and In re Kuhle, 526 F.2d 553, 188 USPQ 7 (CCPA 1975) (deleting a prior art switch member and thereby eliminating its function was an obvious expedient).

Note: Applicant did not argue about claim 2 thus examiner believes that claim 2 stand or fall together with claim1.

(11) Related Proceeding(s) Appendix

No decision rendered by a court or the Board is identified by the examiner in the Related Appeals and Interferences section of this examiner's answer.

For the above reasons, it is believed that the rejections should be sustained.

Respectfully submitted,

Temesghen Ghebretinsae 4/12/07

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